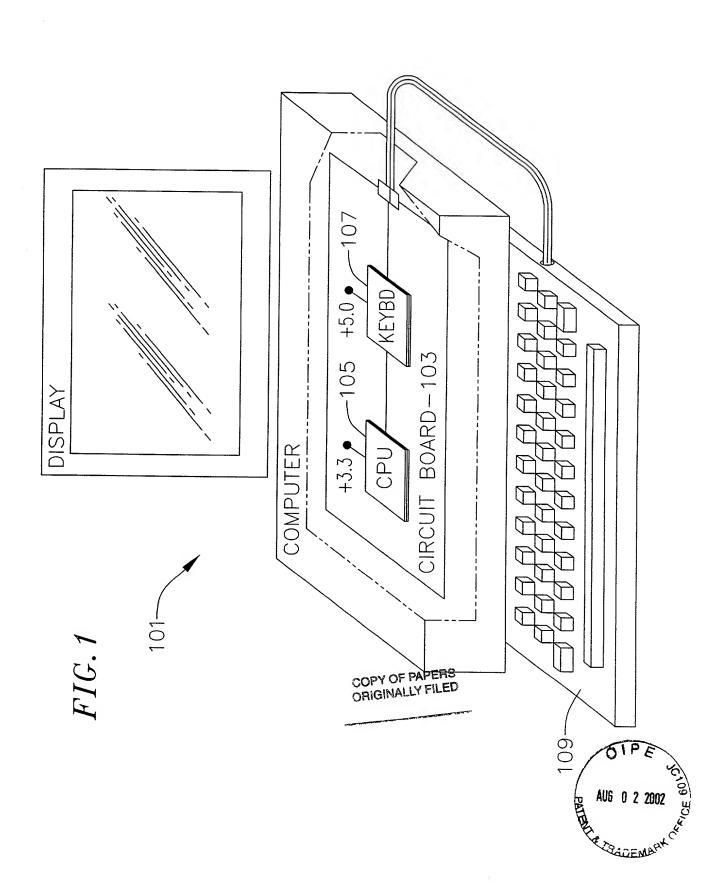
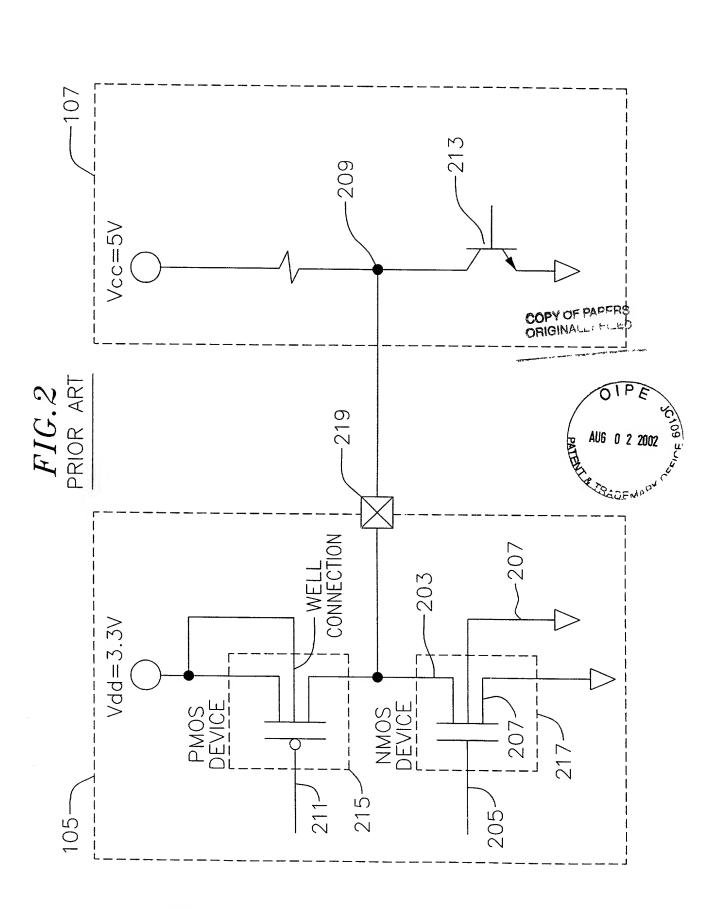
Sheet 1 of 23



Sheet 2 of 23

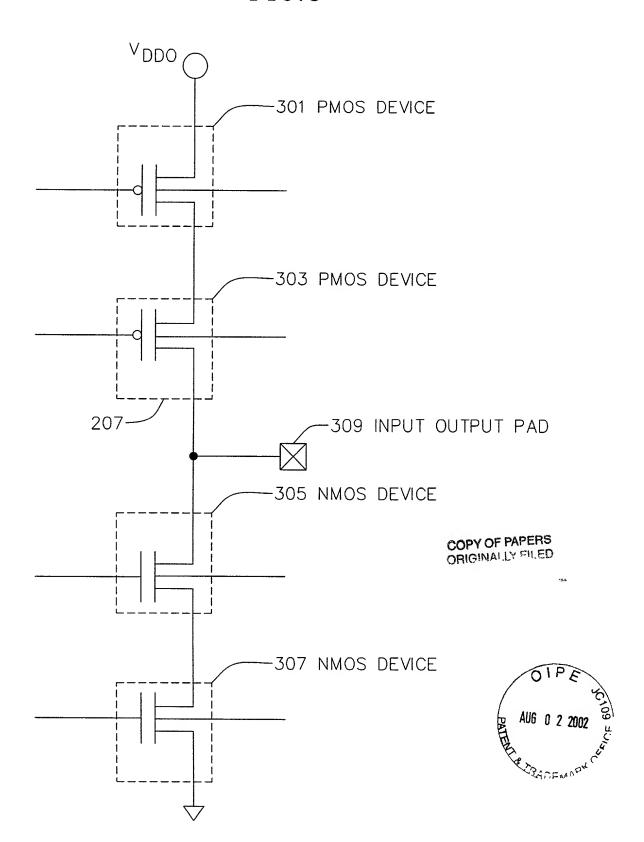


Application No. 10/043,763

Application No. 10/043,763
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

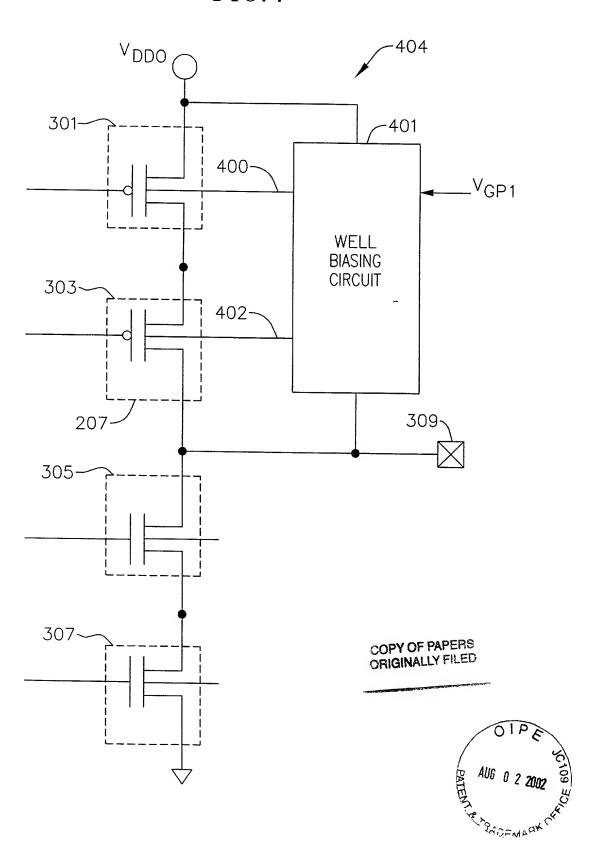
Sheet <u>3</u> of <u>23</u>

FIG.3



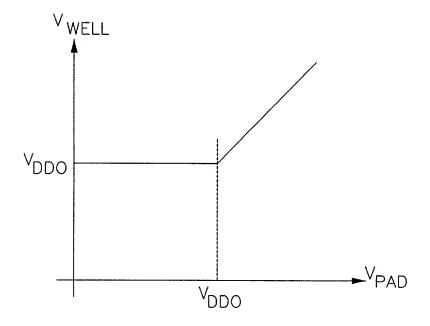
Sheet <u>4</u> of <u>23</u>

FIG.4



Sheet <u>5</u> of <u>23</u>

FIG.5

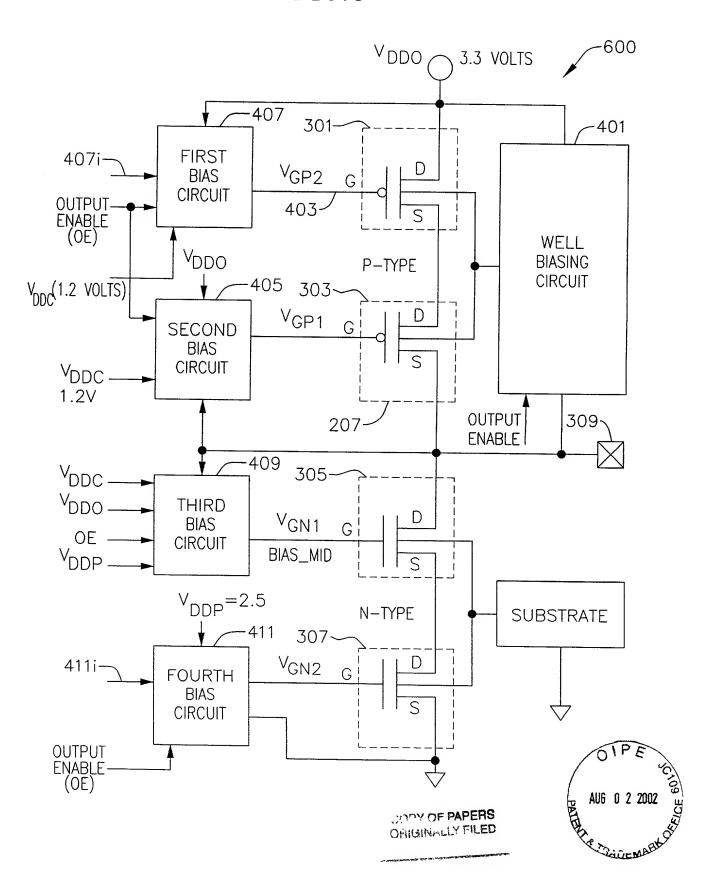


COPY OF PAPERS ORIGINALLY FILED



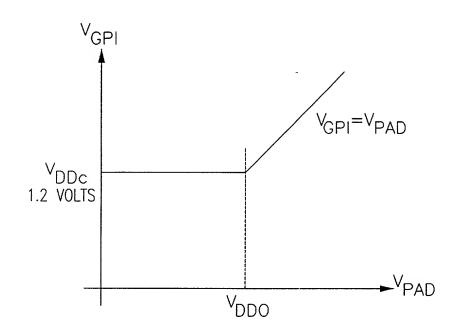
Sheet 6 of 23

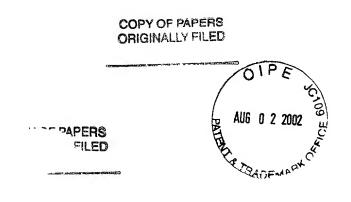
FIG. 6



Sheet <u>7</u> of <u>23</u>

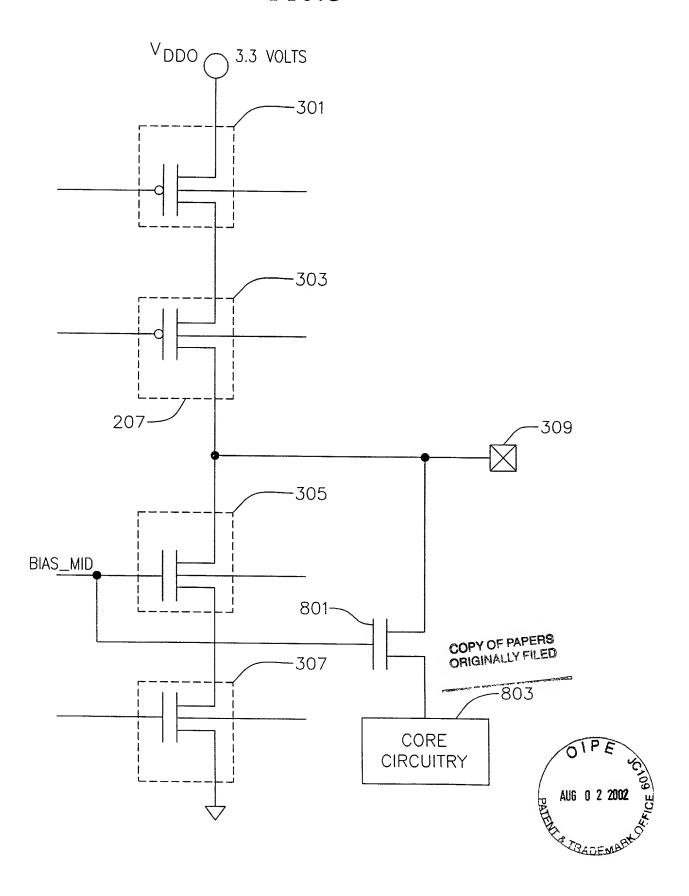
FIG. 7





Sheet <u>8</u> of <u>23</u>

FIG.8



Sheet <u>9</u> of <u>23</u>

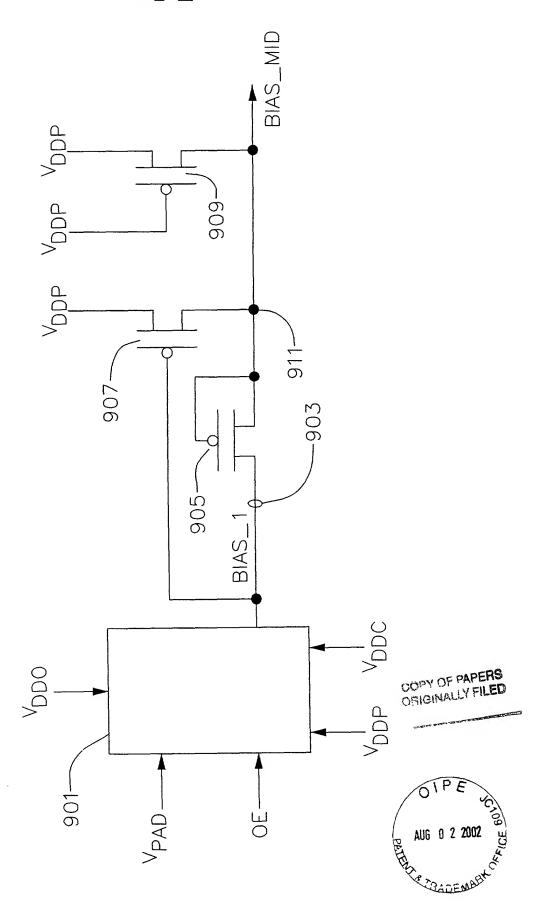
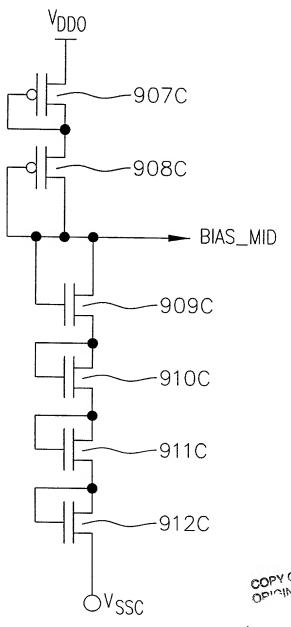


FIG.9A

Sheet <u>10</u> of <u>23</u>

FIG.9B



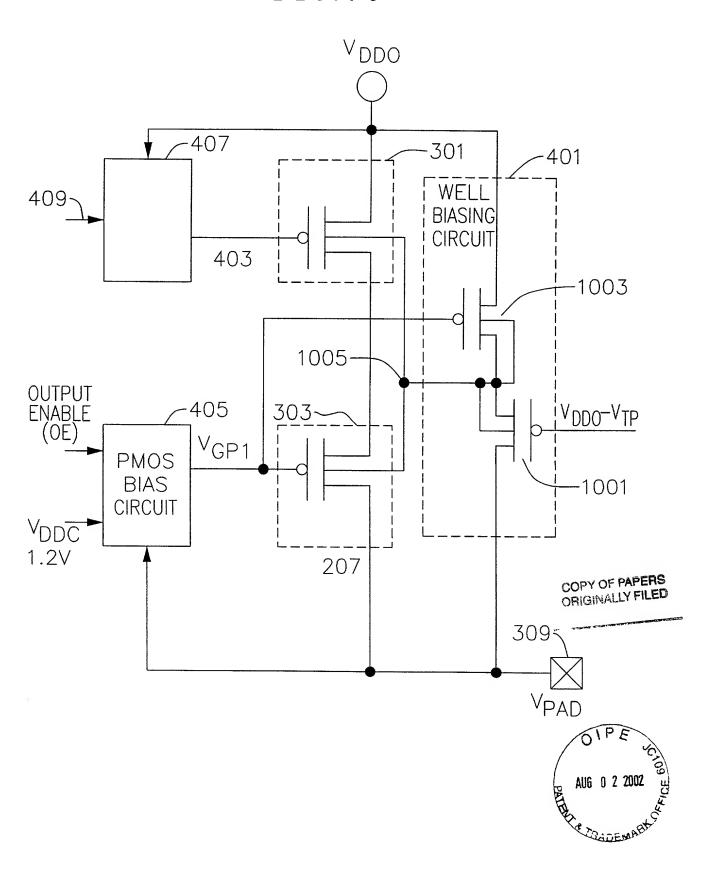
(ALTERNATELY VDDC OR VDDP)

COPY OF PAPERS OPIGINALL FILED



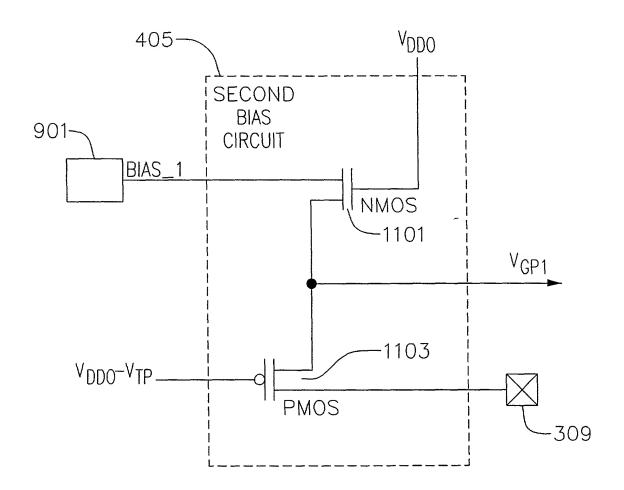
Sheet <u>11</u> of <u>23</u>

FIG. 10



Sheet <u>12</u> of <u>23</u>

FIG. 11A

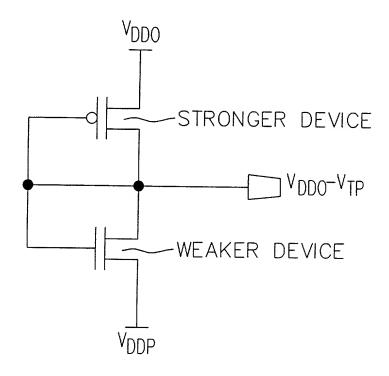






Sheet <u>13</u> of <u>23</u>

FIG. 11B

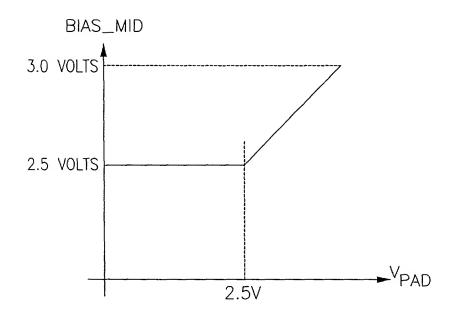






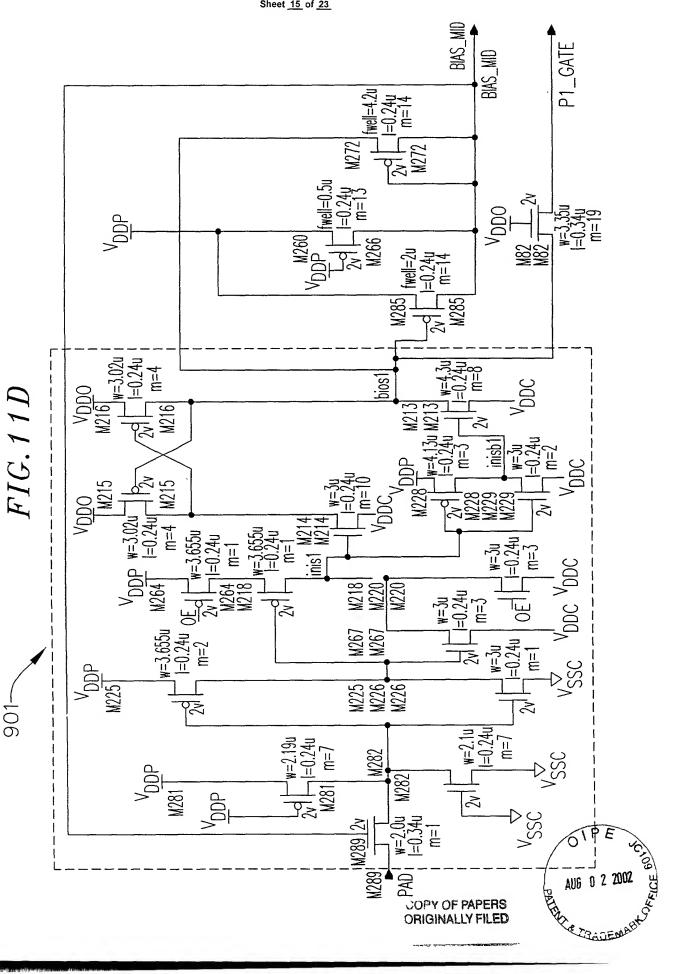
Sheet 14 of 23

FIG. 11C

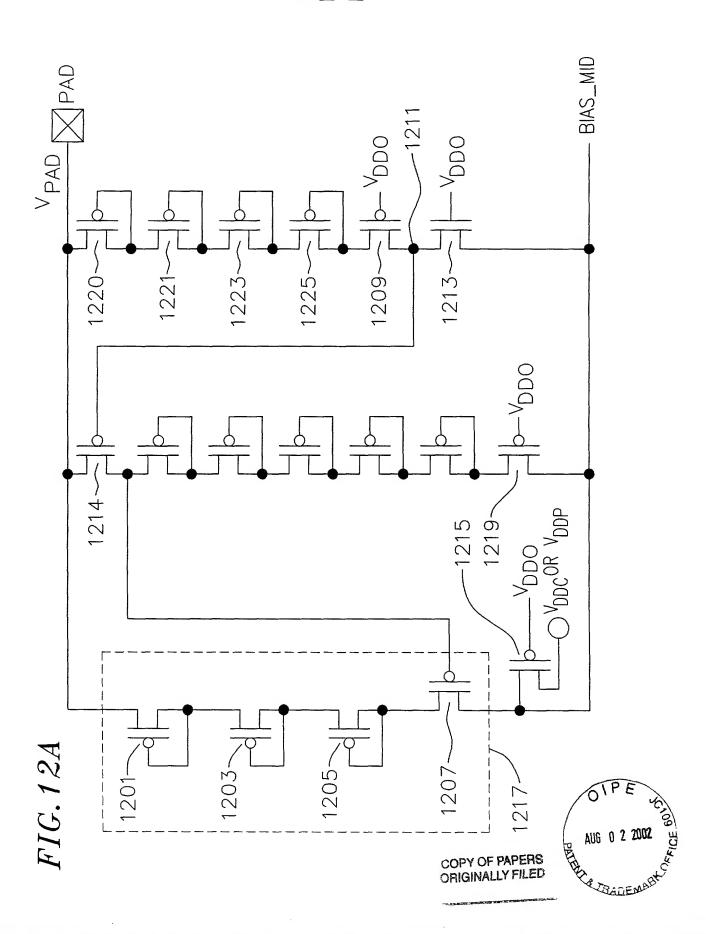




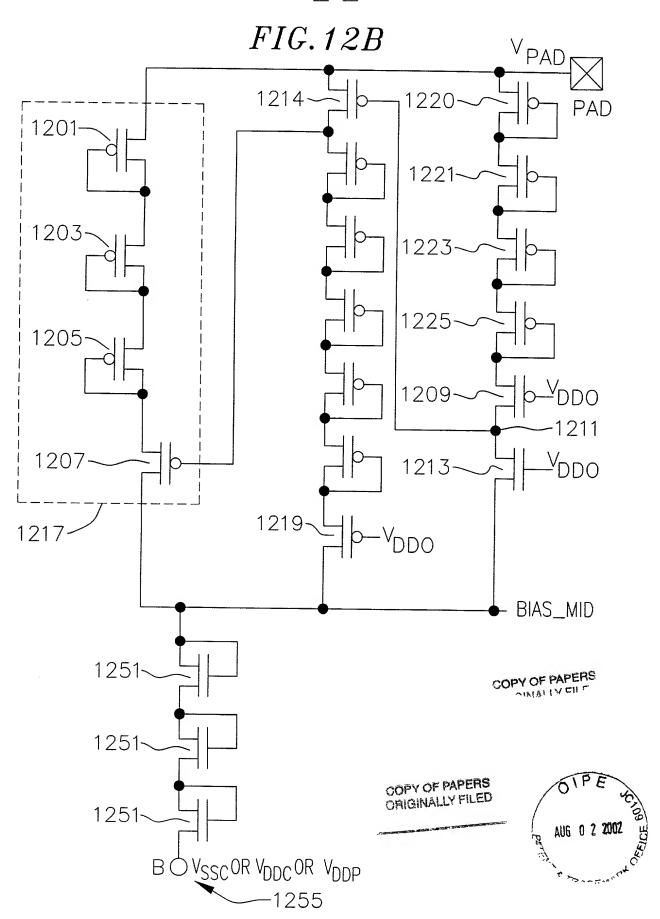
Sheet <u>15</u> of <u>23</u>



Sheet 16 of 23

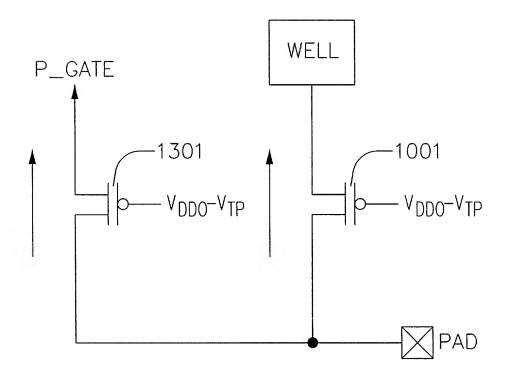


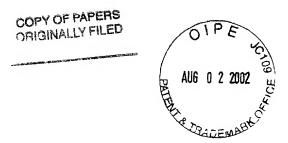
Sheet <u>17</u> of <u>23</u>



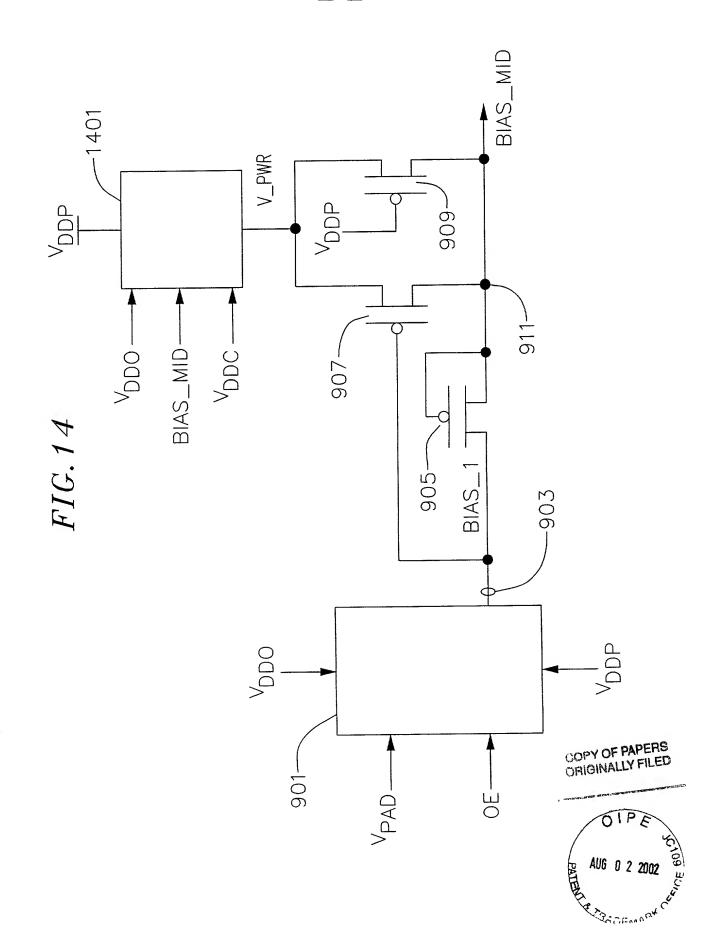
Sheet <u>18</u> of <u>23</u>

FIG. 13





Sheet 19 of 23

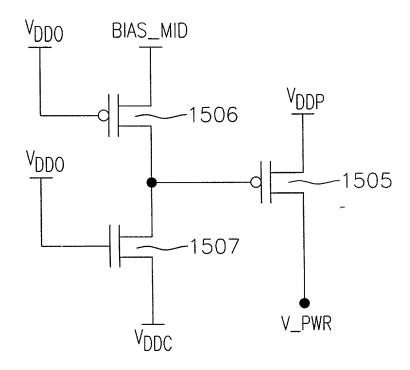


IOOKI/WI OBOTON

Application No. 10/043,763
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

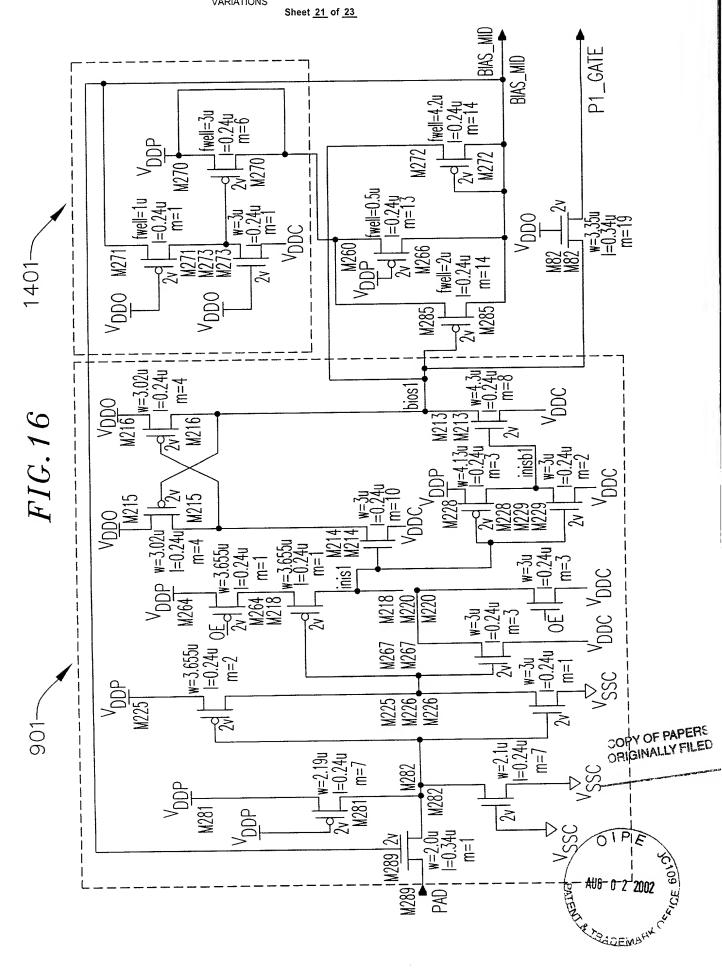
Sheet 20 of 23

FIG. 15

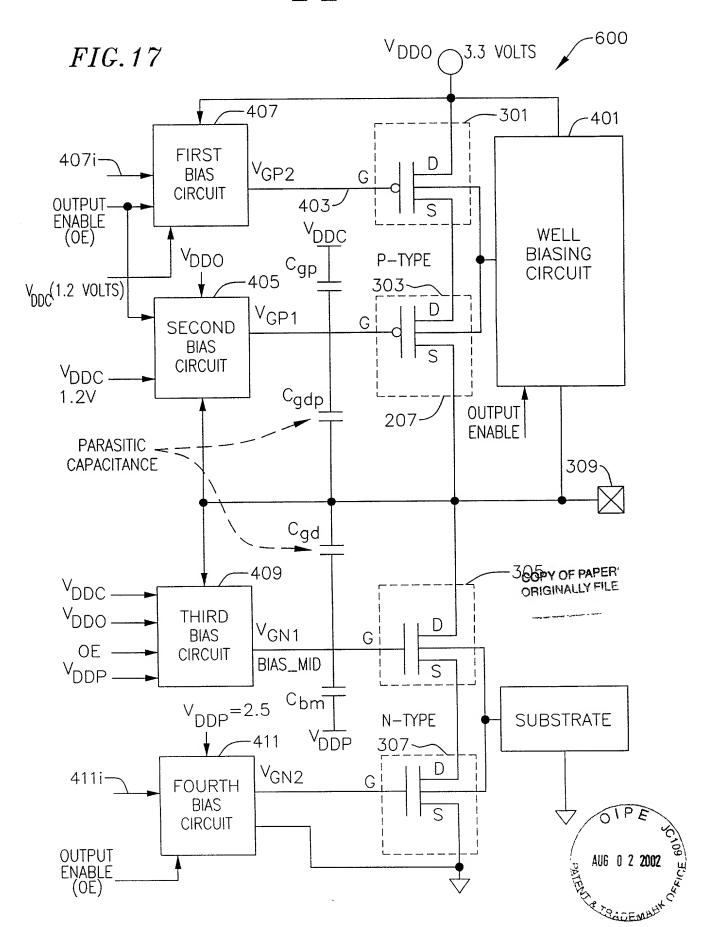


COPY OF PAPERS ORIGINALLY FILED





Sheet 22 of 23



Sheet <u>23</u> of <u>23</u>

FIG. 18

